

Specification for Optrex DMF-50316NF-FW-1

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		First Edition	Approved by	Production Div.			
		June 18, 1998	Checked by	Quality Assurance Div.			
	CD Module Specification	Final Revision					
		*****	Checked by	Design Engineering Div.			
Type No.	DMF-50316NF-FW-1		Prepared by	Production Div.			
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Revision History

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1.General Specifications

Operating Temp.	:	min. $0^{\circ}C \sim max. 50^{\circ}C$
Storage Temp.	:	min20°C ~ max. 60°C
Dot Pixels	:	240 (W) × 64 (H) dots
Dot Size	:	$0.50 (W) \times 0.50 (H) mm$
Dot Pitch	:	0.53 (W) × 0.53 (H) mm
Viewing Area	:	132.0 (W) × 39.0 (H) mm
Outline Dimensions	:	180.0 (W) × 70.0 (H) × 14.0 [*] max. (D) mm * Without CFL Cable
Weight	:	180g max.
LCD Type	:	NTD-12222 (F-STN / Black & White -mode / Transmissive)
Viewing Angle	:	6:00
Control LSI	:	T6963C-0101 (Produced by TOSHIBA)
Data Transfer	:	8-bit data transfer
Backlight	:	Cold Cathode Fluorescent Lamp (CFL) \times 1
Additional Spec.	:	(1) Nega/Posi Reverse Circuit(2) Holder and PWB ground are connected by soldering.
Drawings	:	Dimensional Outline UE-35433

2. Electrical Specifications

2.1.Absolute Maximum Ratings

					Vss=0V
Parameter	Symbol	Conditions	Min.	Max.	Units
Supply Voltage	Vcc-Vss	_	-0.3	7.0	V
(Logic)					
Supply Voltage	VCC-VEE	_	0	28.0	V
(LCD Drive)					
Input Voltage	VI	_	-0.3	Vcc+0.3	V

2.2.DC Characteristics

Ta=25°C, Vss=0V

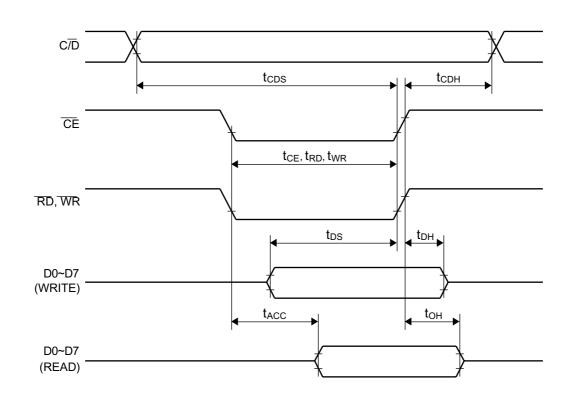
Parameter	Symbol	Conditions	Min.	Тур.	Max.	Units
Supply Voltage	Vcc-Vss	_	4.5	_	5.5	V
(Logic)						
Supply Voltage	VCC-VEE		Shown in 3	.1		V
(LCD Drive)						
High Level	VIH	Vcc=5.0V±10%	Vcc-2.2	_	Vcc	V
Input Voltage						
Low Level	VIL	Vcc=5.0V±10%	0	_	0.8	V
Input Voltage						
High Level	Voh	Iон=-0.75mA	Vcc-0.3	_	Vcc	V
Output Voltage						
Low Level	Vol	Iol=0.75mA	0	-	0.3	V
Output Voltage						
	Icc	Vcc-Vss=5.0V	_	_	20.0	mA
Supply Current						
	IEE	VCC-VEE=13.3V	_	_	10.0	mA

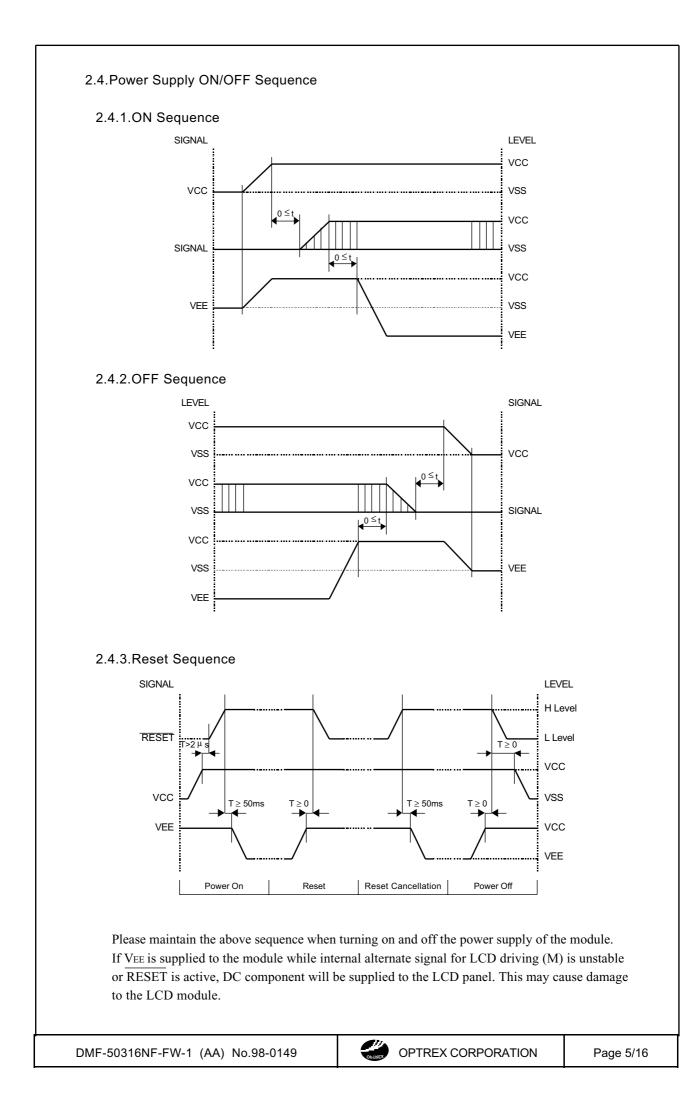
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2.3.AC Characteristics

 $Vcc{=}5.0V{\pm}10\%$

Parameter	Symbol	Min.	Max.	Units
C/D Setup Time	tcds	100	-	ns
C/D Hold Time	tcdh	10	I	ns
$\overline{\text{CE}}, \overline{\text{RD}}, \overline{\text{WR}}$ Pulse Width	t_{CE}, t_{RD}, t_{WR}	80	Ι	ns
Data Setup Time	t _{DS}	80	Ι	ns
Data Hold Time	t dh	40	-	ns
Access Time	tacc	_	150	ns
Output Hold Time	tон	10	50	ns





2.5.Lighting Specifications

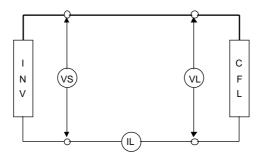
						Ta⁼	=25°C
Parameter	Symbol	Conditions	Min.	Тур.	Max.	Units	Notes
Lamp Voltage	VL	_	_	310	_	Vrms	1
Lamp Current	IL	-	4.0	5.0	6.0	mArms	2
Starting Voltage	Vs	—	_	_	800	Vrms	3
Surface Luminance	L	IL = 5.0 mA	70	130	_	cd/m ²	4
Average Life	Tal	IL = 5.0 mA	8000	10000	_	hrs	5

Note 1 : The voltage (r.m.s.) to maintain the electric discharge of the lamp. It is measured after lighting for 3 minutes .

Note 3 : The voltage at starting the electric discharge when the voltage is increased gradually from 0V.

Note 4 : Surface Luminance is specified by the initial data of luminance measured at the center of display surface after 20 minutes power on. (All ON pattern)

Note 5 : CFL life is defined as the time for which the initial luminance is attenuated by 50% of the luminance value. Average Life representes the time elapsed at the point of time when the residual ratio becomes below 50% when plural lamps are lighted in comparison with the definition of life mentioned above.



CFL Testing Circuit

Note 2 : The current (r.m.s.) to flow through the lamp with the electric discharge. It is measured after lighting for 3 minutes.

3.Optical Specifications

3.1.LCD Driving Voltage

Parameter	Symbol	Conditions	Min.	Тур.	Max.	Units
Recommended		Ta=0°C			16.1	V
LCD Driving Voltage	VCC-VEE	Ta=25°C	12.4	13.3	14.2	V
Note 1		Ta=50°C	11.3		_	V

Note 1 : Voltage (Applied actual waveform to LCD Module) for the best contrast. The range of minimum and maximum shows tolerance of the operating voltage. The specified contrast ratio and response time are not guaranteed over the entire range.

3.2.Optical Characteristics

Ta=25°C, 1/64 Duty, 1/9 Bias, VD=13.3V (Note 4), $\theta = 0^{\circ}, \phi = -^{\circ}$

Pa	rameter	Symbol	Conditions	Min.	Тур.	Max.	Units
Contrast R	atio Note 1	CR	$\theta = 0^{\circ}, \phi = -^{\circ}$		50		
Viewing An	gle			Shown	in 3.3		
Response	Rise Note 2	Ton	_	-	220	330	ms
Time	Decay Note 3	Toff	_	_	80	160	ms

Note 1 : Contrast ratio is definded as follows.

CR = LOFF / LON

LON : Luminance of the ON segments

LOFF : Luminance of the OFF segments

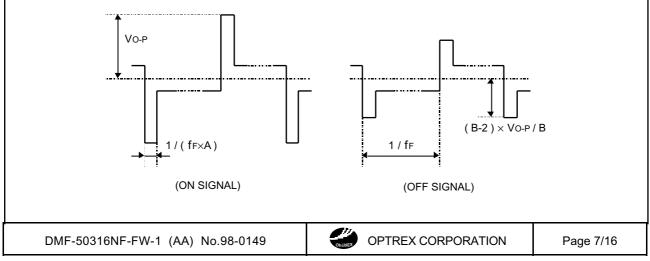
- Note 2 : The time that the luminance level reaches 90% of the saturation level from 0% when ON signal is applied.
- Note 3 : The time that the luminance level reaches 10% of the saturation level from 100% when OFF signal is applied.

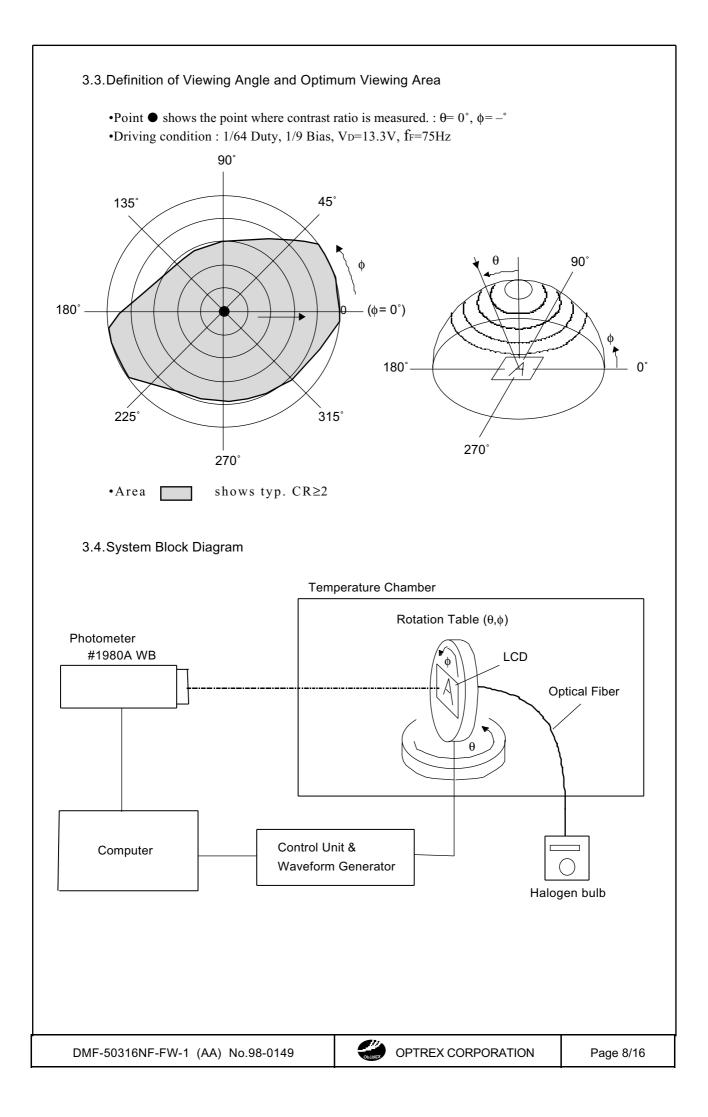
Note 4 : Definition of Driving Voltage VD

Assuming that the typical driving waveforms shown below are applied to the LCD Panel at 1/A Duty - 1/B Bias (A : Duty Number, B : Bias Number). Driving voltage VD is definded as follows.

 $V_D = (Vth1+Vth2) / 2$

- Vth1 :The voltage Vo-P that should provide 50% of the satulation level in the luminance at the segment which the ON signal is applied to.
- Vth2 :The voltage Vo-P that should provide 50% of the satulation level in the luminance at the segment which the OFF signal is applied to.





4.I/O Terminal

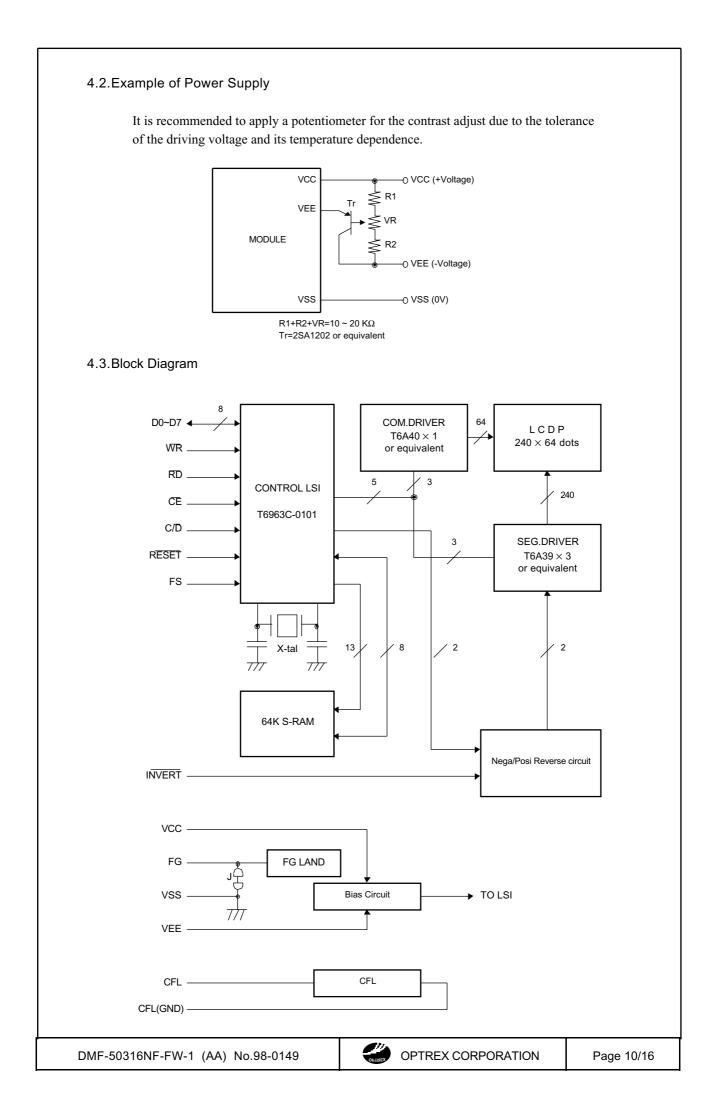
4.1.Pin Assignment

<u>CN1</u>

No.	Symbol	Level	Function
1	FG		Frame Ground
2	Vss	_	Power Supply (0V, GND)
3	Vcc		Power Supply for Logic
4	VEE		Power Supply for LCD Drive
5	WR	H / L	Write Signal L : Read
6	RD	H / L	Read Signal L : Write
7	CE	H / L	Chip Enable Signal L : Enable
8	C/D	H / L	Write Mode H : Command Write L : Data Write
			Read Mode H : Status Read L : Data Read
9	NC		Non-connection
10	RESET	H / L	Reset Signal L : Reset
11	D0	H / L	Data Bus Line
12	D1	H / L	Data Bus Line
13	D2	H / L	Data Bus Line
14	D3	H / L	Data Bus Line
15	D4	H / L	Data Bus Line
16	D5	H / L	Data Bus Line
17	D6	H / L	Data Bus Line
18	D7	H / L	Data Bus Line
19	FS	H / L	H:6 × 8dots L:8 × 8dots (Pulled up by 10 k Ω)
	INVERT	H / L	Display data Reverse signal (Pulled up by 22 k Ω)
			H:Positive Image L:Negative Image

CN2

No.	Symbol	Level	Function
1	CFL	– Power Supply for CFL (HOT)	
2	NC	_	Non-connection
3	NC	-	Non-connection
4	CFL (GND)	_	Power Supply for CFL (GND)



<u>5.Test</u>

No change on display and in operation under the following test condition.

No.	Parameter	Conditions	Notes		
1	High Temperature Operating	50°C±2°C, 96hrs (operation state)			
2	Low Temperature Operating	0°C±2°C, 96hrs (operation state)			
3	High Temperature Storage	60°C±2°C, 96hrs	4		
4	Low Temperature Storage	-20°C±2°C, 96hrs	3, 4		
5	Damp Proof Test	40°C±2°C, 90~95%RH, 96hrs			
6	Vibration Test	Total fixed amplitude : 1.5mm Vibration Frequency : 10~55Hz One cycle 60 seconds to 3 directions of X, Y, Z for each 15 minutes			
7	Shock Test	To be measured after dropping from 60cm high on the concrete surface in packing state. $ \begin{array}{c} $			

Note 1 : Unless otherwise specified, tests will be conducted under the following condition. Temperature $~:20{\pm}5^{\circ}{\rm C}$

Humidity : 65±5%

Note 2 : Unless otherwise specified, tests will be not conducted under functioning state.

Note 3 : No dew condensation to be observed.

Note 4 : The function test shall be conducted after 4 hours storage at the normal temperature and humidity after removed from the test chamber.

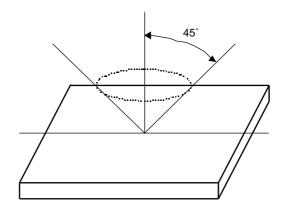
Note 5 : Vibration test will be conducted to the product itself without putting it in a container.

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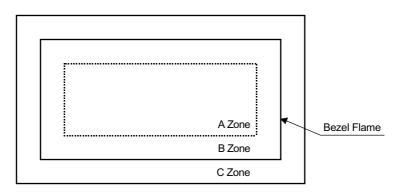
6.Appearance Standards

6.1.Inspection conditions

The LCD shall be inspected under 40W white fluorescent light. The distance between the eyes and the sample shall be more than 30cm. All directions for inspecting the sample should be within 45° against perpendicular line.



6.2. Definition of applicable Zones



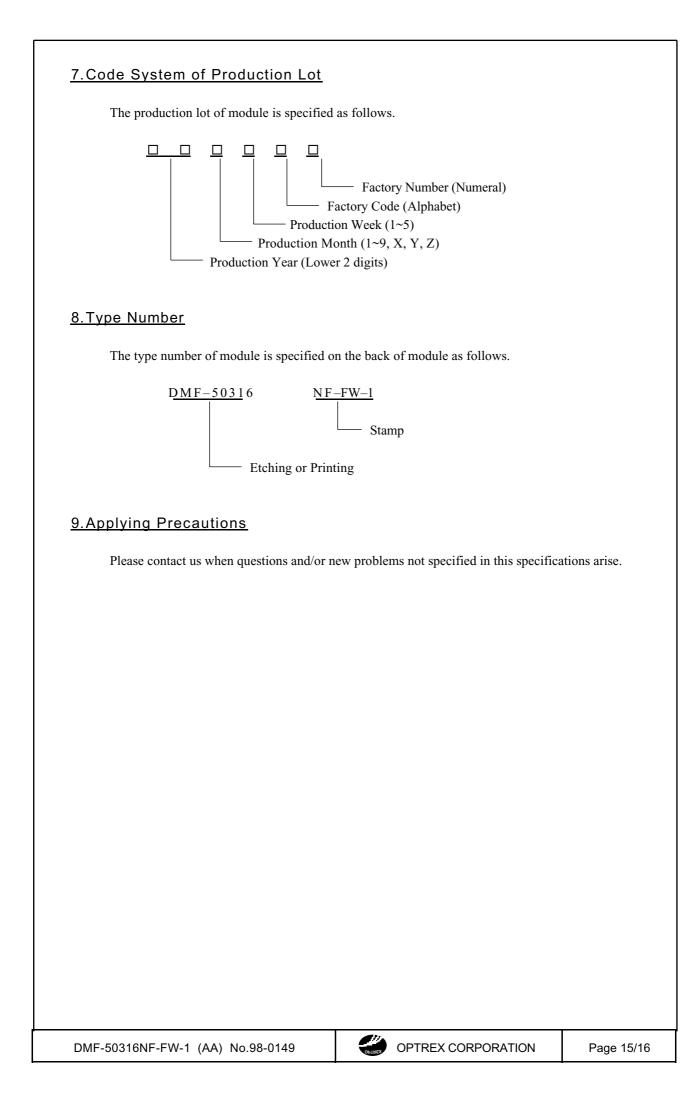
A Zone : Active display area

B Zone : Area from outside of "A Zone" to validity viewing area C Zone : Rest parts

A Zone + B Zone = Validity viewing area

No.						
		(2) Line Shape				
		Zone		Acceptable Number		
		X (mm)	Y (mm)	А	В	С
		_	0.03≥W	*	*	*
		2.0≥L	0.05≥W	3	3	*
		1.0≥L	0.1 ≥W	3	3	*
		_	0.1 <w< td=""><td>In</td><td>the same way</td><td>(1)</td></w<>	In	the same way	(1)
		X : Lengtl				
		Total defects shall not exceed 5.				
2	Air Bubbles					
	(between glass		Zone	Acceptable Number		
	& polarizer)	Dimension	(mm)	А	В	С
		D	≤ 0.3	*	*	*
		0.3 < D	$0 \le 0.4$	3	*	*
		0.4 < D	$0 \le 0.6$	2	3	*
		0.6 < D)	0	0	*
		* : Disrega	rd			
		Total defects shall not exceed 3.				

No.	Parameter	Criteria			
3	The Shape of Dot	(1) Dot Shape (with Dent) 0.152 As per the sketch of left hand.			
		(2) Dot Shape (with Projection)			
		Should not be connected to next dot.			
		(3) Pin Hole			
		(Less than 0.1 mm is no counted.)			
		(4) Deformation $(X+Y) / 2 \le 0.2 \text{mm}$			
		Total acceptable number : 1/dot, 5/cell			
		(Defect number of (4) : 1pc.)			
4	Polarizer Scratches	Not to be conspicuous defects.			
5	Polarizer Dirts	If the stains are removed easily from LCDP surface, the module is not defective.			
6	Complex Foreign Substance Defects	Black spots, line shaped foreign substances or air bubbles between glass & polarizer should be 5pcs maximum in total.			
7	Distance between Different Foreign Substance Defects	$D \le 0.2$: 20mm or more 0.2 < D : 40mm or more			



10. Handling Precautions

Optrex Products are designed for use in ordinary electronic devices such as business machines, telecommunications equipment, measurement devices and etc..

Optrex Products are not designed, intended, or authorized for use in any application in which the failure of the product could result in a situation where personal injury or death may occur. These applications include, but are not limited to, life-sustaining equipment, nuclear control devices, aerospace equipment, devices related to hazardous or flammable materials, etc. (If Buyer intends to purchase or use the Optrex Products for such unintended or unauthorized applications, Buyer must secure prior written consent to such use by a responsible officer of Optrex Corporation.) Should Buyer purchase or use Optrex Products for any such unintended or unauthorized application (without such consent), Buyer shall indemnify and hold Optrex and its officers, employees, subsidiaries, affiliates and distributors harmless against all claims, costs, damages and expenses, and reasonable attorney's fees, arising out of, directly or indirectly, any claim of personal injury or death associated with such unintended or unauthorized use, even if such claim alleges that Optrex was negligent regarding the design or manufacture of the part.

- 1) LCD may be broken because it is made of glass.
- 2) Polarizer is a soft material and can easily be scratched.
- 3) Please avoid static electricity.
- ① Please be sure to ground human body and electric appliances during work.
- ② It is preferable to use conductive mat on table and wear cotton clothes or conduction processed fiber. Synthetic fiber is not recommended.
- ③ Please slowly peel off protective film, because static electricity may be charged.
- 4) If it is necessary to store LCD modules for a long time, please comply with the following procedures. If storage condition is not satisfactory, display (especially polarizer) may be deteriorated or soldering I/O terminals may become difficult (some oxide is generated at I/O terminals plating).
 - ① Store as delivered by Optrex
 - ② If you store as unpacked, put in anti-static bag, seal its opening and store where it is not subjected to direct sunshine nor fluorescent lamp.
 - ③ Store at temperature 0 to +35°C and at low humidity. Please refer to our specification sheets for storage temperature range and humidity condition.
- The module does not contain excess current limiter.
 Please design the limiter to cut excess current in your power supply circuit.
- 6) Liquid crystal may be leaked when display is broken. Never taste it. If your hands or clothes touch it, please immediately wash using soap.
- 8) A high voltage over 1000V is applied at the connector of CFL cable when the CFL inverter is connected and energized. Please do not touch there incidentally or accidentally to avoid a skin burn. And please set the cable properly in the housing to avoid a worn-out of isolated cover of cable wire.

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